

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): MIURA et al.	Atty. Dkt.: 01-583
Serial No.: Unknown	Group Art Unit:
Filed: Concurrently herewith	Examiner:
Title: SEMICONDUCTOR DEVICE	

Commissioner for Patents  
Arlington, VA 22202

Date: March 25, 2004

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the reference(s) listed on the attached Form PTO-1449 is/are submitted for consideration by the Examiner without any admission that it/they constitute(s) statutory prior art, or without any admission that it/they contain(s) subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed reference(s) has/have been considered and made of record.

Respectfully submitted,



David G. Posz  
Reg. No. 37,701

Posz & Bethards, PLC  
11250 Roger Bacon Drive, Suite 10  
Reston, VA 20190  
(703)707-9110 (phone)  
Customer No. 23400

FORM PTO-1449	ATTY. DKT NO.	01-583	SER. NO.
	APPLICANT	MIURA et al.	
	FILING DATE	March 25, 2004	GROUP

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
		2003/0132530 A1 (Discussed in page 1 of the spec.)	Jul. 17, 2003	Teshima et al.		
		6,255,672 B1	Jul. 3, 2001	Yoshida et al.		

## FOREIGN PATENT DOCUMENTS

## TRANSLATION

		DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS	YES	NO	
										Eng. Abstract
		JP-A-2002-164485	6/7/02	Japan				X (Partial)		
		JP-A-2000-174180	6/23/00	Japan				X (Partial)		

\* Full English text is available in machine-translated form in JPO (Japanese Patent Office) English language web site at <http://www1.ipdl.jpo.go.jp/PA1/cgi-bin/PA1INDEX>.

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

EXAMINER		DATE CONSIDERED